L Number	Hits	Search Text	DB	Time stamp
-	1683	716/1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 10:16
-	715	716/7	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 10:15
-	1243	716/8	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 10:15
-	589	716/9	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 10:16
-	0	(716/1).ccls. and (M1 same ((power adj supply) adj (bus or wiring)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 10:18
-	0	(716/7).ccls. and (M1 same ((power adj supply) adj (bus or wiring)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 10:18
-	0	(716/8).ccls. and (M1 same ((power adj supply) adj (bus or wiring)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 10:19
-	0	(716/9).ccls. and (M1 same ((power adj supply) adj (bus or wiring)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 10:19
-	0	(716/$).ccls. and (M1 same ((power adj supply) adj (bus or wiring)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 10:19
-	21	(M1 same ((power adj supply) adj (bus or wiring)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 10:29
-	9	((M1 adj layer) same (power adj supply))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 10:43
-	6829	(power adj supply) and ASIC	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 10:44

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-	1	((power adj supply) adj bus) same ASIC	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 10:45
-	260	(multiple same ((power adj supply) adj voltages))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 11:59
-	0	ASIC same (multiple adj ((power adj supply) adj voltages))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 12:00
-	6	ASIC and (multiple adj ((power adj supply) adj voltages))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 12:00
-	17040	(bus adj structure) amd (ASIC adj (device adj image))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 14:10
-	0	(bus adj structure) and (ASIC adj (device adj image))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 14:10
-	11	(bus adj structure) and (ASIC and (device adj image))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 14:20
-	0	M1 and (bus adj structure) and (ASIC and (device adj image))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 14:11
-	0	(logic adj blocks) same (filer adj cell)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 14:21
-	2	(logic adj blocks) and (filler adj cell)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 14:41
-	0	(logic adj block) same (filler adj cell)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 14:27
-	217	(logic adj block) same (IP macro)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 14:28

-	7	ASIC same (logic adj block) same (IP macro)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 14:28
-	3	(blocks) same (filler adj cell)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 14:43
-	0	(different adj voltage) same (filler adj cell)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 14:46
-	0	(different adj voltages) same (filler adj cell)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 14:46
-	0	(logic adj block) same (IP adj macro)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 15:25
-	0	custom adj (IP adj macro)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 15:32
-	21	IP adj macro	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 15:52
-	0	ASIC same (IP adj macro)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 15:52
-	0	ASIC and (IP adj macro)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 15:53
-	2	ASIC and ((intellectual adj property) adj macro)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 15:54
-	54	(multiple adj ((power adj supply) adj voltages))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/19 17:35

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